

Serial No. 09/838,166

Docket No.: 1095.1184

**REMARKS**

The Title and claim 1, 3, and 4 have been amended.

Claims 4 and 5 have been cancelled and new claims 7 and 8 have been added. Support for the claim amendments and new claims is found for example, on page 17, line 19 – page 19, line 24 of the specification.

Claims 1-4 and 7-8 are now pending and under consideration. Reconsideration is respectively requested.

**I. IN THE TITLE:**

In accordance with the foregoing, the Title has been amended to recite "AN INFORMATION PROCESSING METHOD AND RECORDING MEDIUM THEREFOR CAPABLE OF ENHANCING THE EXECUTING SPEED OF A PARALLEL PROCESSING COMPUTING DEVICE". Thus, it is respectfully submitted that the objection is overcome.

**II. OBJECTION OF CLAIMS 2, 5 AND 6:**

As previously mentioned above, claims 5 and 6 have been cancelled and claim 2 has been amended to overcome the objection. Therefore, it is respectfully submitted that the objection is overcome.

**III. REJECTION OF CLAIMS 1-4 UNDER 35 U.S.C. § 112, SECOND PARAGRAPH:**

As previously mentioned above, claims 1-4 have been amended to overcome this rejection. Therefore, it is respectfully submitted that the rejection is overcome.

**IV. REJECTION OF CLAIMS 1-6 UNDER 35 U.S.C. § 102(b) AS BEING ANTICIPATED BY KAMETANI (US PATENT NO. 5,481,747):**

Claim 1 is amended to recite "...comparing a first parallel block number of a parallel processing control information region and a second parallel block number of a thread information region, and determining whether a corresponding thread should execute said next

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parallel processing block, wherein when execution is required, determining said next parallel processing block to be executed with reference to said second parallel block number, and when execution is not required, generating a parallel processing block control information region to said next parallel processing block". It is respectfully submitted that Kametani does not disclose or suggest such features.

Although the above comments are specifically directed to claim 1, it is respectfully submitted that the comments would be helpful in understanding differences of various other rejected claims over the cited reference. Therefore, it is respectfully submitted that the rejection is overcome.

**V. REJECTION OF CLAIMS 3-6 UNDER 35 U.S.C. § 102(b) AS BEING ANTICIPATED BY APPLICANTS ADMITTED PRIOR ART (HEREINAFTER "APA"):**

Claim 3 has been amended to recite "...comparing means for comparing a first parallel block number of a parallel processing control information region and a second parallel block number of a thread information region, and determining means for determining whether a corresponding thread should execute said next parallel processing block, wherein when execution is required, determining said next parallel processing block to be executed with reference to said second parallel block number, and when execution is not required, generating a parallel processing block control information region to said next parallel processing block".

Claim 4 has been amended to recite "...comparing means for comparing a first parallel block number of a parallel processing control information region and a second parallel block number of a thread information region, and determining means for determining whether a corresponding thread should execute said next parallel processing block, wherein when execution is required, determining said next parallel processing block to be executed with reference to said second parallel block number, and when execution is not required, generating a parallel processing block control information region to said next parallel processing block".

It is respectfully submitted that the APA does not disclose or suggest such features. Therefore, it is respectfully submitted that the rejection is overcome.

**VI. REJECTION OF CLAIMS 1-6 UNDER 35 U.S.C. § 102(b) AS BEING ANTICIPATED BY MURAMATSU ET AL. (US PATENT NO. 5,043,873; HEREINAFTER "MURAMATSU"):**

It is respectfully submitted that Muramatsu fails to disclose all of the features as recited in amended claims 1, 3 and 4 as mentioned above. Therefore, claims 1, 3 and 4 also patentably

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distinguish over Muramatsu. In addition, claim 2 also patentably distinguishes over Muramatsu at least due to its dependency upon claim 1. Therefore, it is respectfully submitted that the rejection is overcome.

**VII. REJECTION OF CLAIMS 1-6 UNDER 35 U.S.C. § 102(b) AS BEING ANTICIPATED BY MOTOMURA (US PATENT NO. 5,815,727):**

Motomura fails to teach or suggest all of the features recited in amended claims 1, 3 and 4 as mentioned above. Therefore, claims 1, 3 and 4 patentably distinguish over Motomura. Therefore, it is respectfully submitted that the rejection is overcome.

**VIII. REJECTION OF CLAIMS 1 AND 3-5 UNDER 35 U.S.C. § 102(e) AS BEING ANTICIPATED BY AOKI ET AL. (JP2001167060; HEREINAFTER "AOKI"):**

The Applicants respectfully submit that Aoki does not qualify as prior art. The filing date of the subject application is April 20, 2001 and Aoki has a publication date of June 22, 2001. Thus, withdrawal of the rejection of claims 1 and 3-5 based upon Aoki is respectfully requested.

**IX. CONCLUSION**

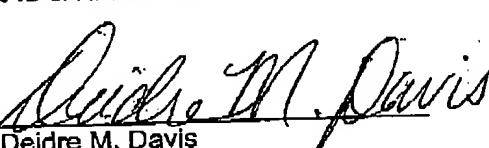
In view of the foregoing amendments and remarks, it is respectfully submitted that each of the claims patentably distinguishes over the prior art, and therefore, defines allowable subject matter. A prompt and favorable reconsideration of the rejection along with an indication of allowability of all pending claims are therefore respectfully requested.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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on 8-19-04

STAAS & HALSEY

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Date 8-19-04